

## Description

DLLCxxI a 3.3V~ 24V uni-directional TVS diode, utilizing leading monolithic silicon technology to provide fast response time and low ESD clamping voltage, making this device an ideal solution for protecting voltage sensitive high-speed data lines. The DLLCxxI has a low capacitance with a typical value at 1pF, and complies with the IEC 61000-4-2 (ESD) standard with  $\pm 15\text{kV}$  air and  $\pm 8\text{kV}$  contact discharge. It is assembled into a lead-free SOD-323 package. The small size, low capacitance and high ESD surge protection make DLLCxxI an ideal choice to protect cell phone, wireless systems, and communication equipment.

## Features

- ◆ 350W peak pulse power (8/20 $\mu\text{s}$ )
- ◆ Ultra low capacitance : 1.0pF typical
- ◆ Ultra low leakage: nA level
- ◆ Low Operating: 3.3V, 5V, 8V, 12V, 15V, 18V, 24V
- ◆ Low clamping voltage
- ◆ Protects one power line or data line
- ◆ Complies with following standards:
  - IEC 61000-4-2 (ESD) immunity test
    - Air discharge:  $\pm 30\text{kV}$
    - Contact discharge:  $\pm 30\text{kV}$
  - IEC61000-4-4 (EFT) 40A (5/50ns)
- ◆ RoHS Compliant

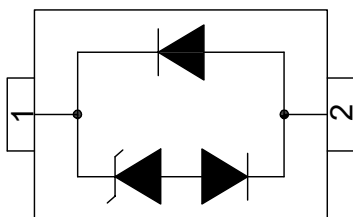
## Mechanical Characteristics

- ◆ Package: SOD-323
- ◆ Lead Finish: Matte Tin
- ◆ Case Material: "Green" Molding Compound.
- ◆ UL Flammability Classification Rating 94V-0
- ◆ Moisture Sensitivity: Level 3 per J-STD-020
- ◆ Terminal Connections: See Diagram Below
- ◆ Marking Information: See Below

## Applications

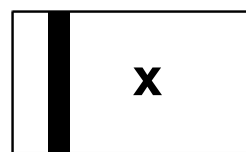
- ◆ USB Ports
- ◆ Smart Phones
- ◆ Wireless Systems
- ◆ Ethernet 10/100/1000 Base T

## Dimensions and Pin Configuration



Circuit and Pin Schematic

## Marking Information



x: Device Marking Code

## Ordering Information

Part Number	Marking	Packaging	Reel Size
DLLCxxI	x	3000/Tape & Reel	7 inch

### Absolute Maximum Ratings ( $T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
ESD per IEC 61000-4-2 (Air)	V <sub>ESD</sub>	±30	kV
ESD per IEC 61000-4-2 (Contact)		±30	
Operating Temperature Range	T <sub>J</sub>	-40 to +85	°C
Storage Temperature Range	T <sub>stg</sub>	-55 to +150	°C

### Electrical Characteristics ( $T_A=25^\circ\text{C}$ unless otherwise specified)

DLLC03I (Marking Code: C)						
Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	V <sub>RWM</sub>			3.3	V	
Breakdown Voltage	V <sub>BR</sub>	4			V	I <sub>T</sub> = 1mA
Reverse Leakage Current	I <sub>R</sub>		1	100	nA	V <sub>RWM</sub> = 3.3V
Forward Voltage	V <sub>F</sub>		0.8	1.2	V	I <sub>F</sub> = 10mA
Clamping Voltage	V <sub>C</sub>			9	V	I <sub>PP</sub> = 1A (8 x 20µs pulse)
Clamping Voltage	V <sub>C</sub>			14	V	I <sub>PP</sub> = 18A (8 x 20µs pulse)
Peak Pulse Current	I <sub>PP</sub>			18	A	t <sub>p</sub> = 8/20µs
Junction Capacitance	C <sub>J</sub>		1		pF	V <sub>R</sub> = 0V, f = 1MHz

<b>DLLC05I (Marking Code: A)</b>						
<b>Parameter</b>	<b>Symbol</b>	<b>Min</b>	<b>Typ</b>	<b>Max</b>	<b>Unit</b>	<b>Test Condition</b>
Reverse Working Voltage	$V_{RWM}$			5	V	
Breakdown Voltage	$V_{BR}$	6			V	$I_T = 1\text{mA}$
Reverse Leakage Current	$I_R$		1	100	nA	$V_{RWM} = 5\text{V}$
Forward Voltage	$V_F$		0.8	1.2	V	$I_F = 10\text{mA}$
Clamping Voltage	$V_C$			10	V	$I_{PP} = 1\text{A}$ (8 x 20 $\mu\text{s}$ pulse)
Clamping Voltage	$V_C$			16	V	$I_{PP} = 16\text{A}$ (8 x 20 $\mu\text{s}$ pulse)
Peak Pulse Current	$I_{PP}$			16	A	$t_p = 8/20\mu\text{s}$
Junction Capacitance	$C_J$		1		pF	$V_R = 0\text{V}$ , $f = 1\text{MHz}$

<b>DLLC08I (Marking Code: B)</b>						
<b>Parameter</b>	<b>Symbol</b>	<b>Min</b>	<b>Typ</b>	<b>Max</b>	<b>Unit</b>	<b>Test Condition</b>
Reverse Working Voltage	$V_{RWM}$			8	V	
Breakdown Voltage	$V_{BR}$	8.5			V	$I_T = 1\text{mA}$
Reverse Leakage Current	$I_R$		1	100	nA	$V_{RWM} = 8\text{V}$
Forward Voltage	$V_F$		0.8	1.2	V	$I_F = 10\text{mA}$
Clamping Voltage	$V_C$			14	V	$I_{PP} = 1\text{A}$ (8 x 20 $\mu\text{s}$ pulse)
Clamping Voltage	$V_C$			19	V	$I_{PP} = 13\text{A}$ (8 x 20 $\mu\text{s}$ pulse)
Peak Pulse Current	$I_{PP}$			13	A	$t_p = 8/20\mu\text{s}$
Junction Capacitance	$C_J$		1		pF	$V_R = 0\text{V}$ , $f = 1\text{MHz}$

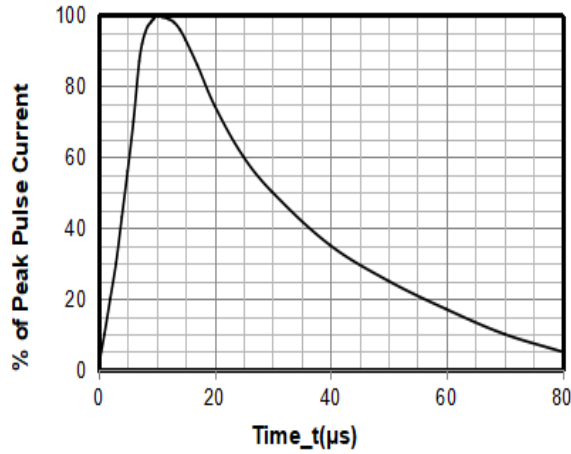
DLLC12I (Marking Code: D)						
Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	$V_{RWM}$			12	V	
Breakdown Voltage	$V_{BR}$	13.3			V	$I_T = 1\text{mA}$
Reverse Leakage Current	$I_R$		1	100	nA	$V_{RWM} = 12\text{V}$
Forward Voltage	$V_F$		0.8	1.2	V	$I_F = 10\text{mA}$
Clamping Voltage	$V_C$			19	V	$I_{PP} = 1\text{A}$ (8 x 20 $\mu\text{s}$ pulse)
Clamping Voltage	$V_C$			25	V	$I_{PP} = 10\text{A}$ (8 x 20 $\mu\text{s}$ pulse)
Peak Pulse Current	$I_{PP}$			10	A	$t_p = 8/20\mu\text{s}$
Junction Capacitance	$C_J$		1		pF	$V_R = 0\text{V}$ , $f = 1\text{MHz}$

DLLC15I (Marking Code: E)						
Parameter	Symbol	Min	Typ	Max	Unit	Test Condition
Reverse Working Voltage	$V_{RWM}$			15	V	
Breakdown Voltage	$V_{BR}$	16.7			V	$I_T = 1\text{mA}$
Reverse Leakage Current	$I_R$		1	100	nA	$V_{RWM} = 15\text{V}$
Forward Voltage	$V_F$		0.8	1.2	V	$I_F = 10\text{mA}$
Clamping Voltage	$V_C$			20	V	$I_{PP} = 1\text{A}$ (8 x 20 $\mu\text{s}$ pulse)
Clamping Voltage	$V_C$			31	V	$I_{PP} = 8\text{A}$ (8 x 20 $\mu\text{s}$ pulse)
Peak Pulse Current	$I_{PP}$			8	A	$t_p = 8/20\mu\text{s}$
Junction Capacitance	$C_J$		1		pF	$V_R = 0\text{V}$ , $f = 1\text{MHz}$

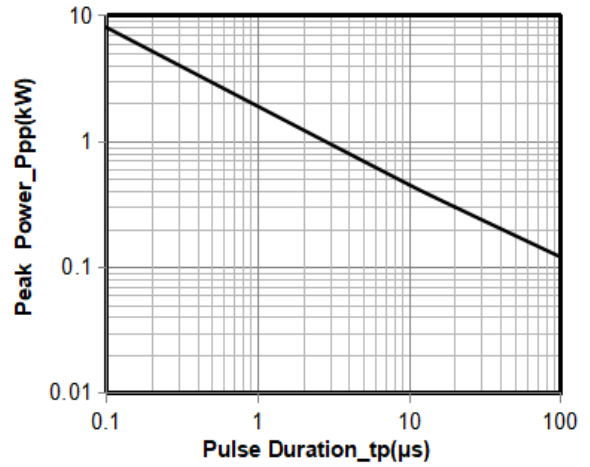
<b>DLLC18I (Marking Code: F)</b>						
<b>Parameter</b>	<b>Symbol</b>	<b>Min</b>	<b>Typ</b>	<b>Max</b>	<b>Unit</b>	<b>Test Condition</b>
Reverse Working Voltage	VRWM			18	V	
Breakdown Voltage	VBR	20			V	IT = 1mA
Reverse Leakage Current	IR		1	100	nA	VRWM = 18V
Forward Voltage	VF		0.8	1.2	V	IF=10mA
Clamping Voltage	VC			29	V	I <sub>PP</sub> = 1A (8 x 20µs pulse)
Clamping Voltage	VC			71	V	I <sub>PP</sub> = 5A (8 x 20µs pulse)
Peak Pulse Current	I <sub>PP</sub>			5	A	tp=8/20µs
Junction Capacitance	CJ		1		pF	VR = 0V, f = 1MHz

<b>DLLC24I (Marking Code: H)</b>						
<b>Parameter</b>	<b>Symbol</b>	<b>Min</b>	<b>Typ</b>	<b>Max</b>	<b>Unit</b>	<b>Test Condition</b>
Reverse Working Voltage	VRWM			24	V	
Breakdown Voltage	VBR	26.7			V	IT = 1mA
Reverse Leakage Current	IR		1	100	nA	VRWM = 24V
Forward Voltage	VF		0.8	1.2	V	IF=10mA
Clamping Voltage	VC			40	V	I <sub>PP</sub> = 1A (8 x 20µs pulse)
Clamping Voltage	VC			71	V	I <sub>PP</sub> = 3.5A (8 x 20µs pulse)
Peak Pulse Current	I <sub>PP</sub>			3.5	A	tp=8/20µs
Junction Capacitance	CJ		1		pF	VR = 0V, f = 1MHz

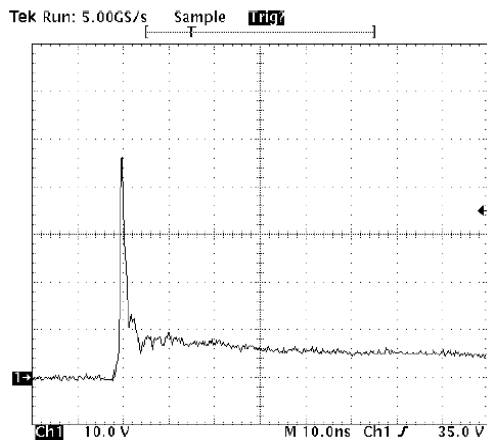
**Typical Performance Characteristics (TA=25°C unless otherwise Specified)**



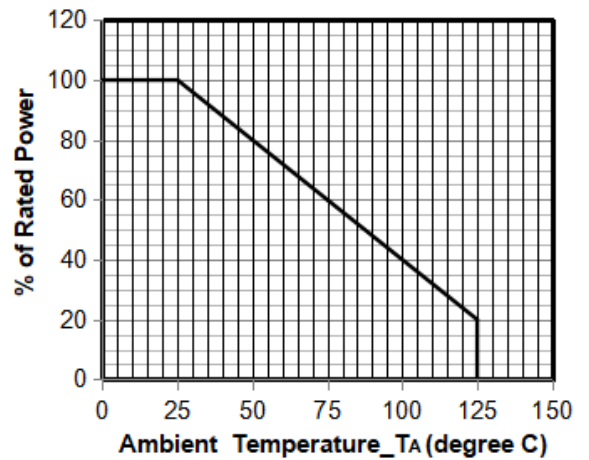
**8 X 20μs Pulse Waveform**



**Peak Pulse Power vs. Pulse Time**

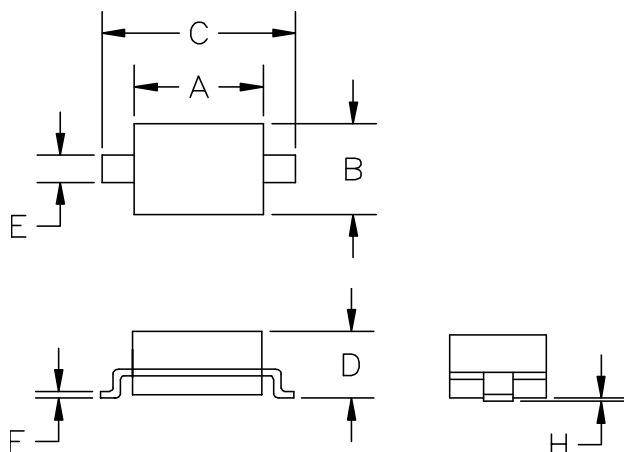


**Note: Data is taken with a 10x attenuator**  
**ESD Clamping Voltage**  
**8 kV Contact per IEC61000-4-2**



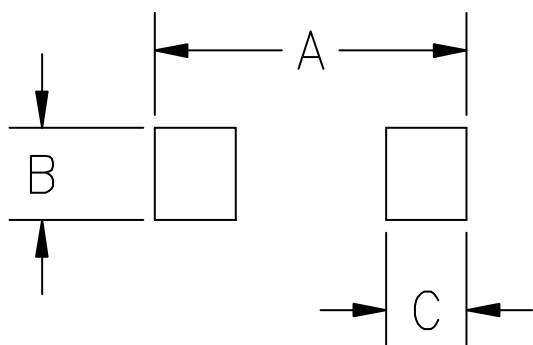
**Power Derating Curve**

## SOD-323 Package Outline Drawing



SYM	DIMENSIONS			
	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.50	1.80	0.060	0.071
B	1.20	1.40	0.045	0.054
C	2.30	2.70	0.090	0.107
D	-	1.10	-	0.043
E	0.30	0.40	0.012	0.016
F	0.10	0.25	0.004	0.010
H	-	0.10	-	0.004

## Suggested Land Pattern



SYM	DIMENSIONS	
	MILLIMETERS	INCHES
A	3.15	0.120
B	0.80	0.031
C	0.80	0.031

## Contact Information

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